

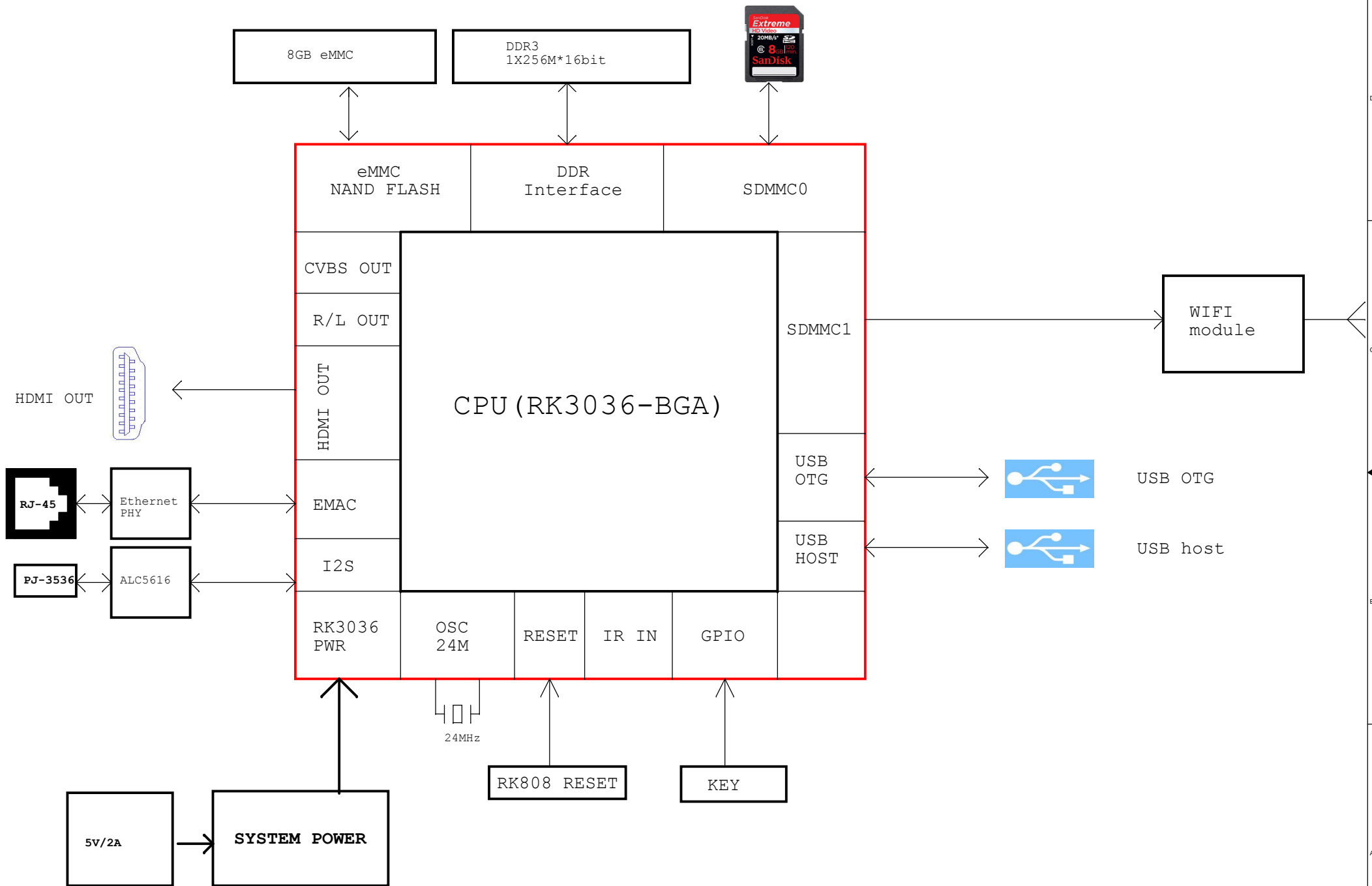
CONTENT INDEXING

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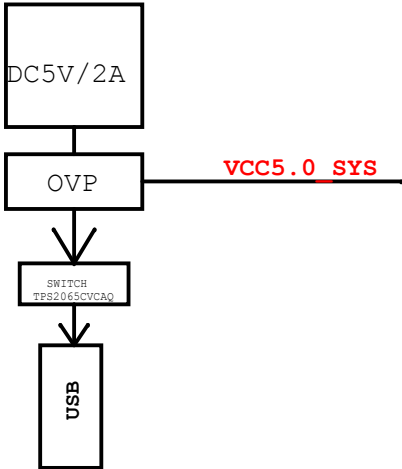
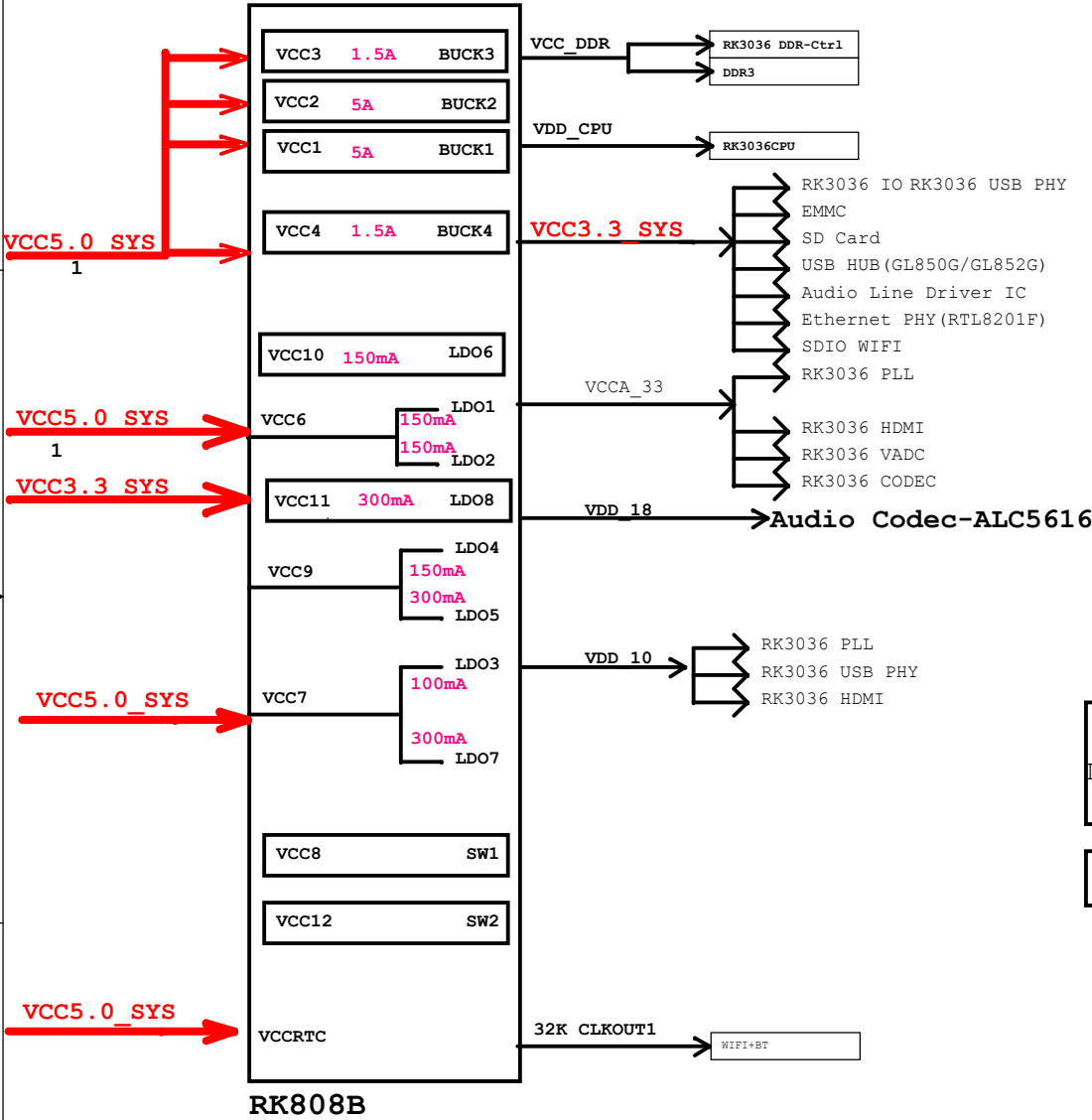
4 LAYERS PCB STACK

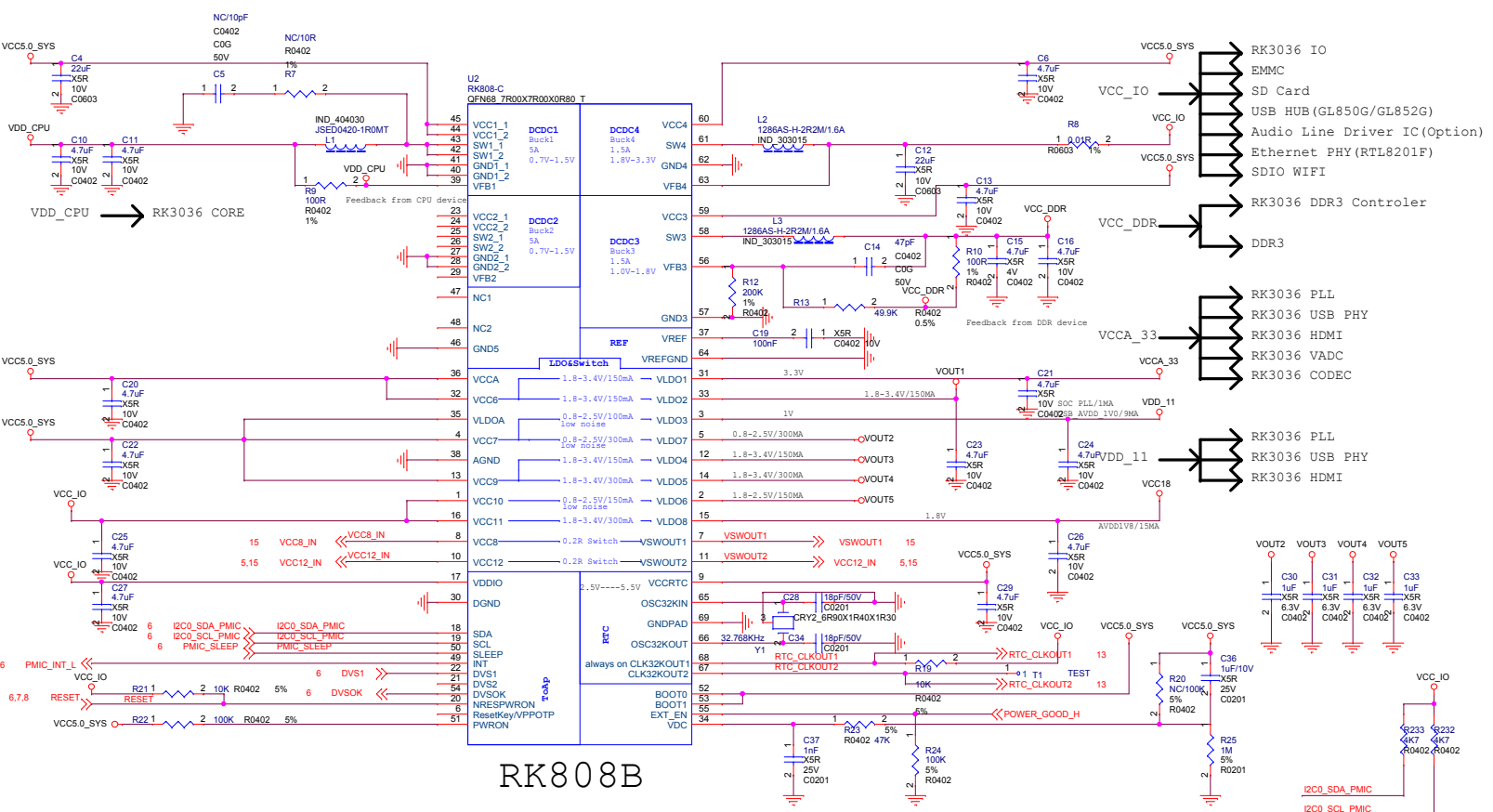
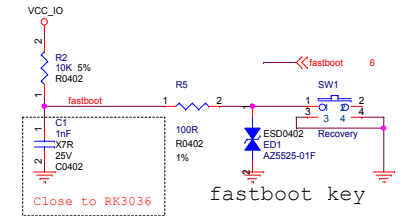
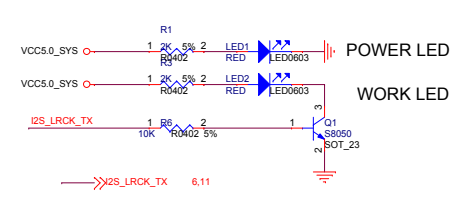
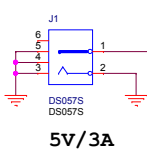
TOP	<div><div></div><div>Prepreg 3313*1 4.0MIL(0.10mm)</div><div></div></div>	Hoz(18um) + plating copper(18um)
GND	<div><div></div><div></div><div>Adjust</div></div>	1oz(35um)
POWER	<div><div></div><div>Prepreg 3313*1 4.0MIL(0.10mm)</div><div></div></div>	1oz(35um)
BOTTOM	<div><div></div><div></div><div></div></div>	Hoz(18um) + plating copper(18um)

Version	Date	Author	Change Note	Note
V1.0	20151021			
V2.0	20151210		1. ADD RK808 I2C pull-up resistor R232 R233 2. CHANGE I2C2_SCL_AUDIO I2C2_SDA_AUDIO 3. DELETE RK808 PWRON KEY AND RESET KEY 4. Change VCCA_33 on resistance R40 to DCDC4 output(VCC_IO/ Fixed 3.3V output) 5. USB OTG -----MICRO USB 6. Change power interface from USB MIRCRO to universal power supply port 7. Update socket footprint 8. Update PJ-3536 footprint 9. Update pcb antenna footprint 10.C183 changed to 1210 chip package 11.Add the pull on resistor in sd	

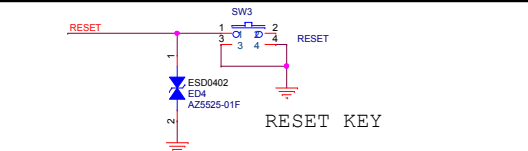
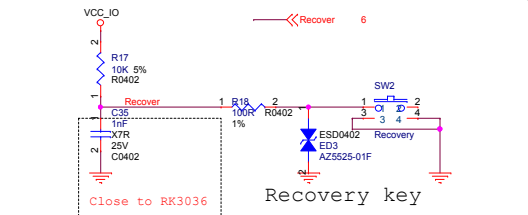
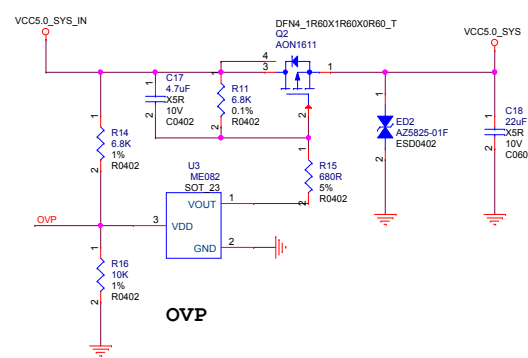
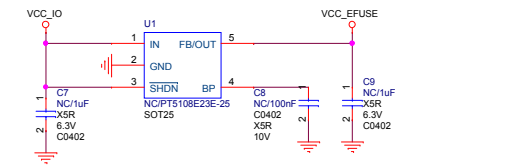


	Power On Sequence	Preset Voltage	Power On Sequence	Preset Voltage	Power On Sequence	Preset Voltage	Power On Sequence	Preset Voltage
Boot1, Boot0	00		01		10		11	
							B*	C*
BUCK1	4	1.1V/ON	4	1.2V/ON	4	1.0V/ON	2	2
BUCK2	5	1.1V/ON	5	1.2V/ON	4	1.0V/ON	3	3
BUCK3	2	X*/ON	2	X*/ON	3	X*/ON	4	4
BUCK4	1	3.0V/ON	1	3.0V/ON	1	3.0V/ON	7	6
LDO1		3.3V/OFF		3.3V/OFF	1	3.3V/ON	6	7
LDO2		3.3V/OFF	2	3.3V/ON		3.3V/OFF		
LDO3	3	1.1V/ON	3	1.2V/ON	2	1.0V/ON	1	1
LDO4	3	2.5V/ON		2.5V/OFF	2	1.8V/ON		
LDO5		2.8V/OFF		2.8V/OFF		2.8V/OFF	8	
LDO6		1.2V/OFF		1.2V/OFF		1.2V/OFF		
LDO7		1.8V/OFF		1.8V/OFF		1.8V/OFF	5	
LDO8		3.3V/OFF		1.8V/OFF		3.3V/OFF		
SWITCH1	1	3.0V/ON	1	3.0V/ON	5	3.0V/ON	8	
SWITCH2		3.0V/OFF		3.0V/OFF		3.0V/OFF		

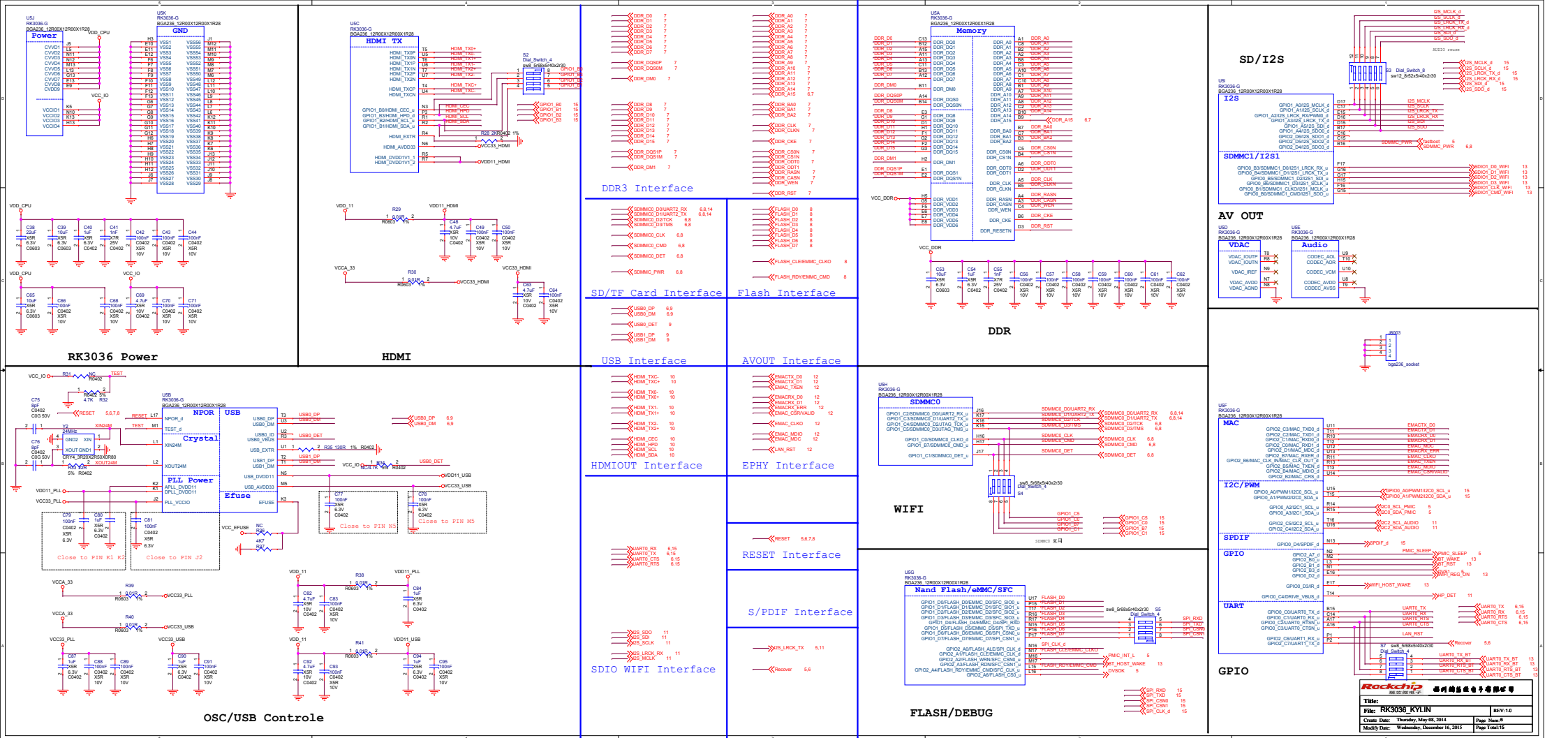


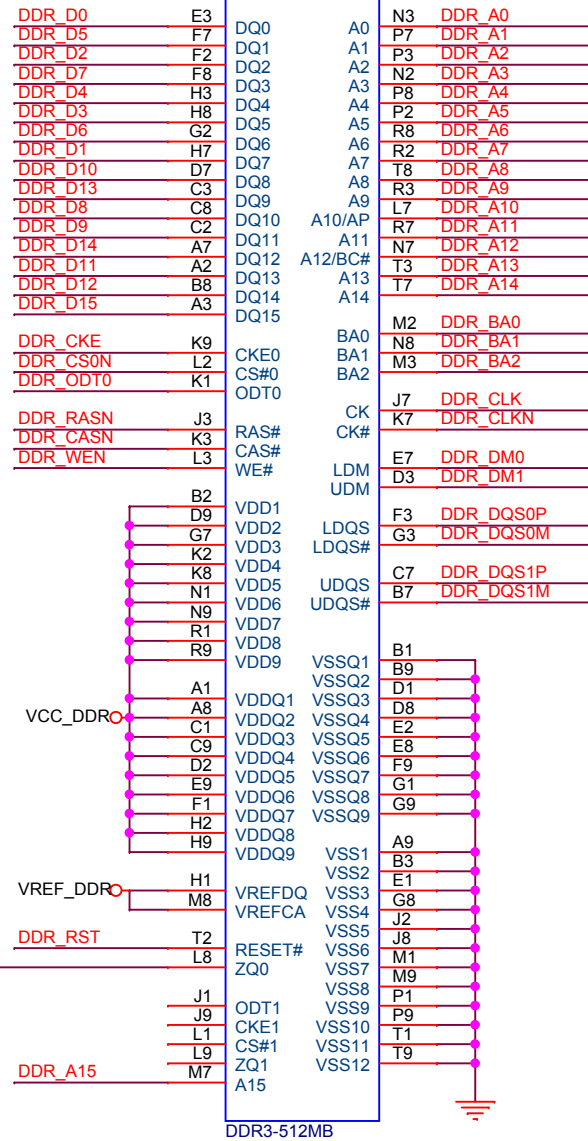
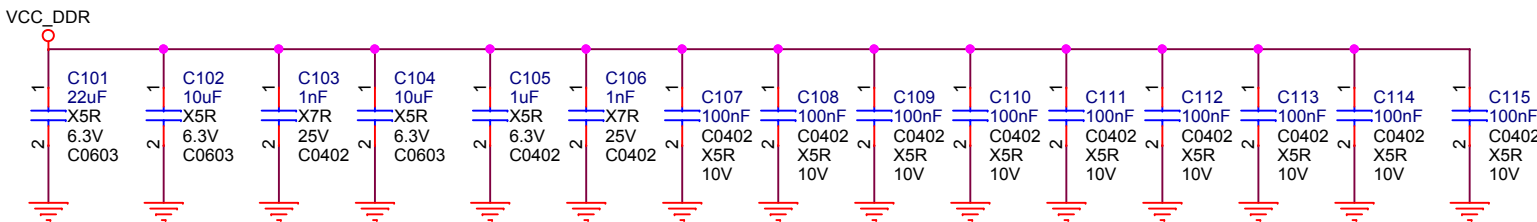
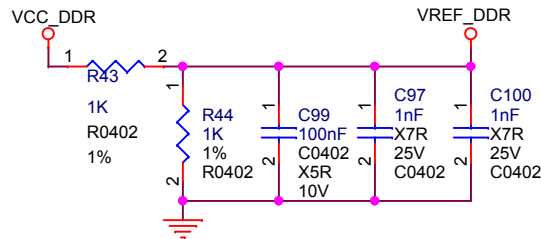
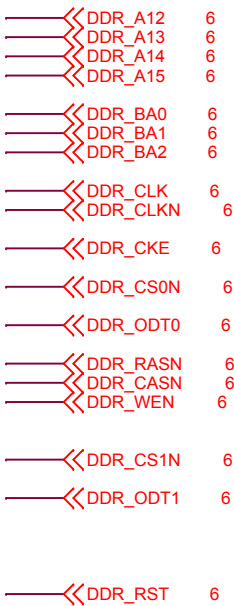
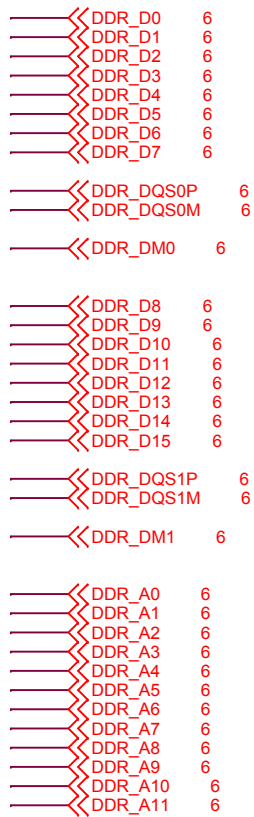


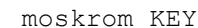
RK808B

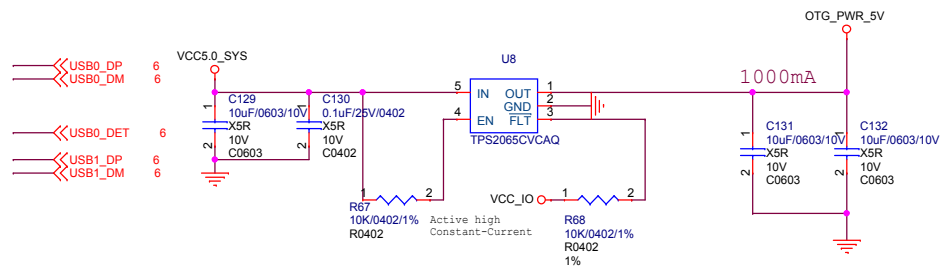


Rackchip 瑞芯微电子 福州瑞芯微电子有限公司	
Title: RK3036_KYLIN	
Create Date: Tuesday, July 03, 2012	Page Num: 5
Modify Date: Wednesday, December 16, 2015	Page Total: 15

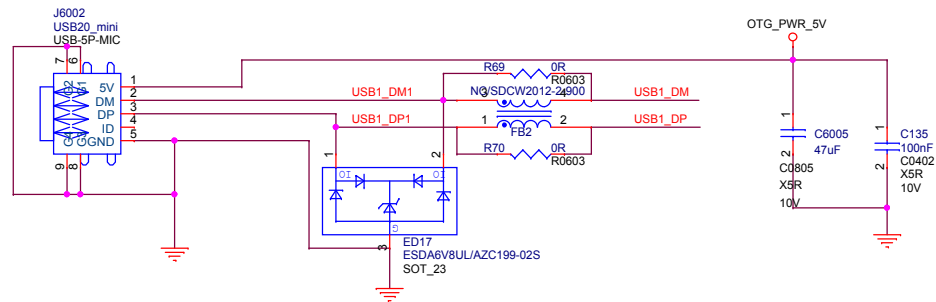
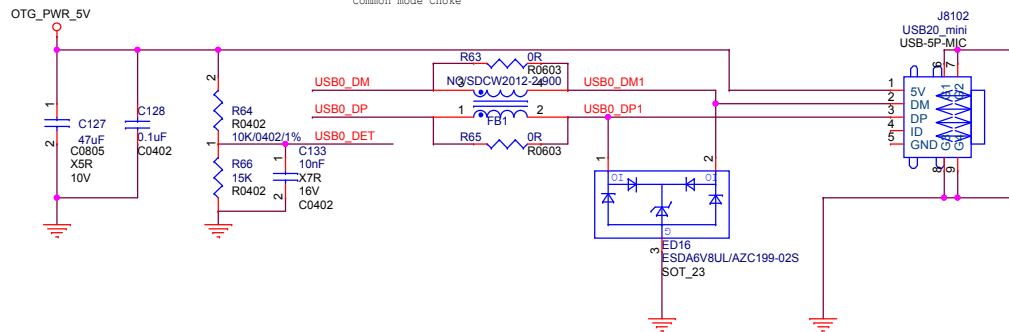


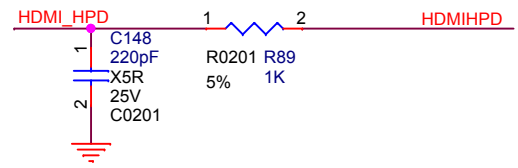
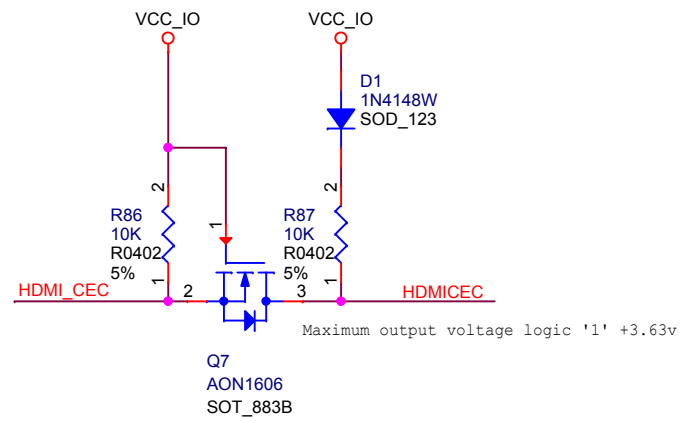
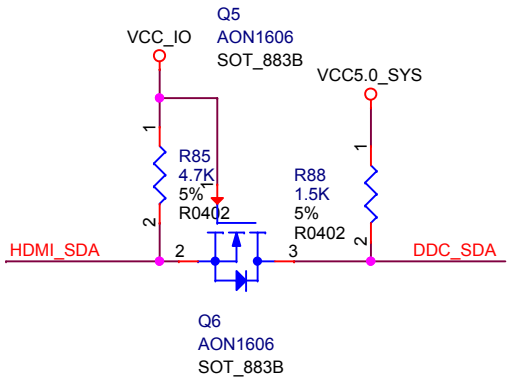
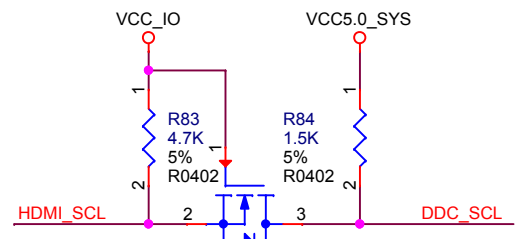
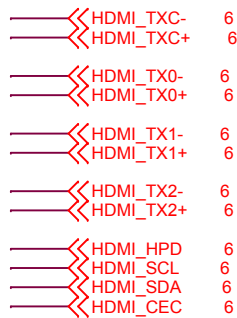




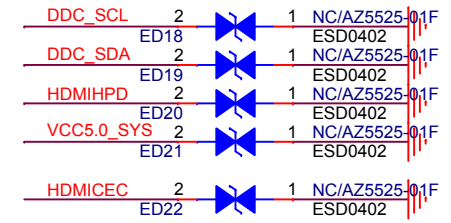
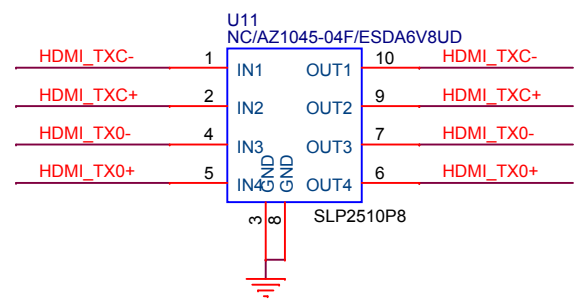
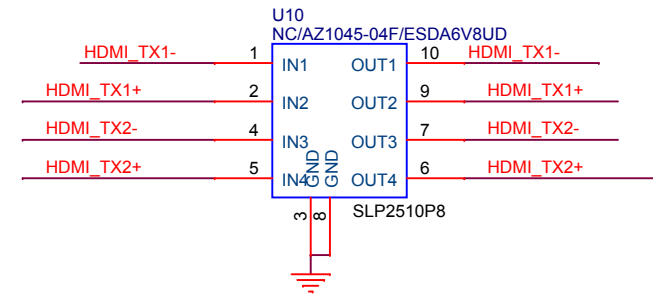
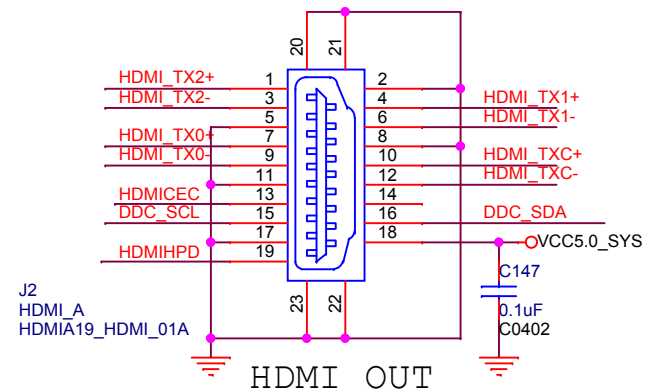


If consider EMI, which requires soldering
Common mode Choke





HDMI-detect





福州瑞芯微电子有限公司

Title: HDMI OUT

File: RK3036_KYLIN

Create Date: Monday, July 02, 2012

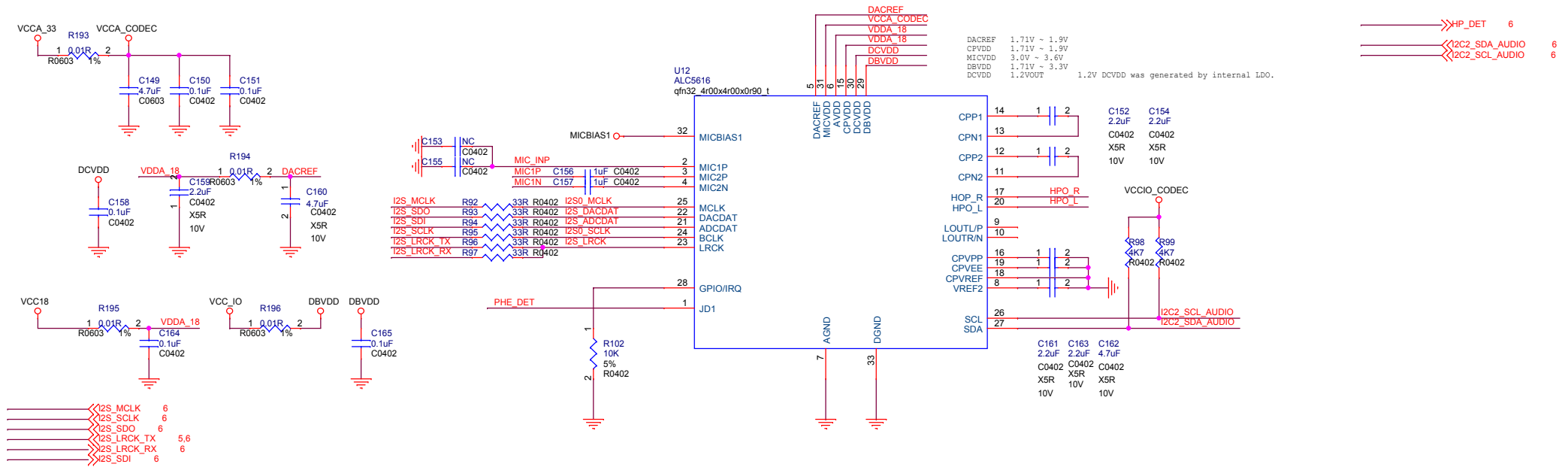
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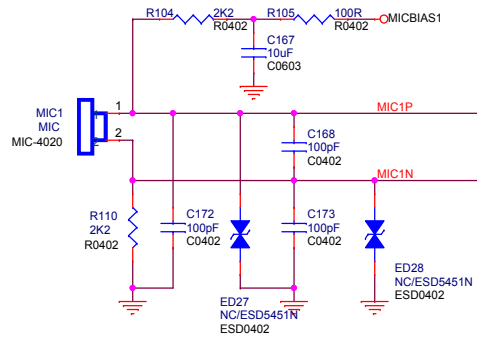
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REV:1.0

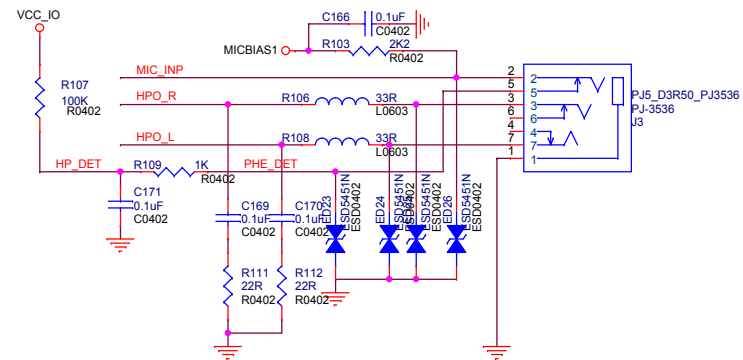
CODEC

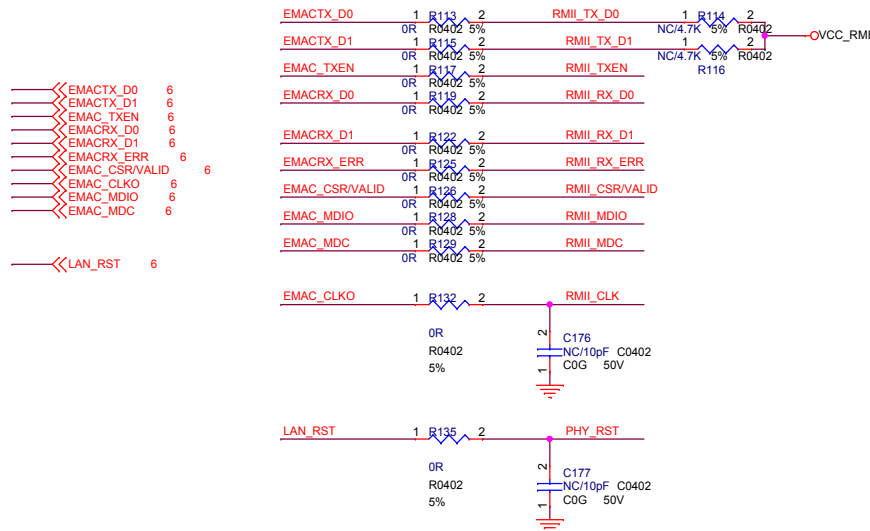


MIC

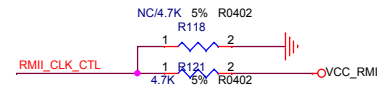


EARPHONE



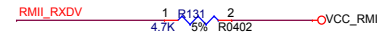


RMII REF_CLK direction



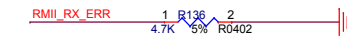
Pull Low for RMII REF_CLK Output mode
Pull High for RMII REF_CLK Input mode(default)

MII/RMII Selection



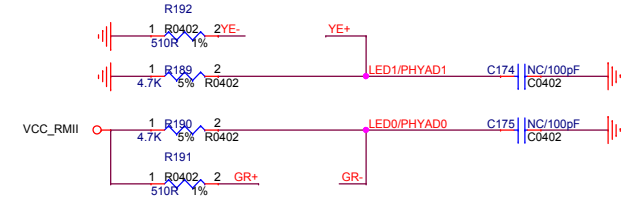
Pull High for RMII mode(default)

UTP / Fiber Selection

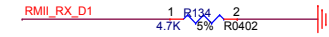


Pull Low for UTP Mode(default)
Pull High for Fiber Mode

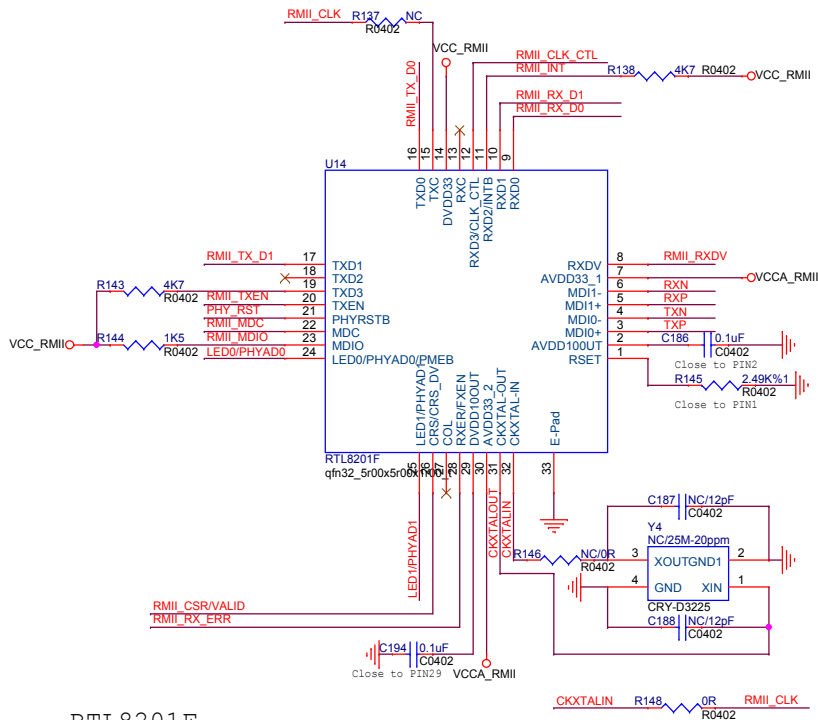
PHY Address/LED

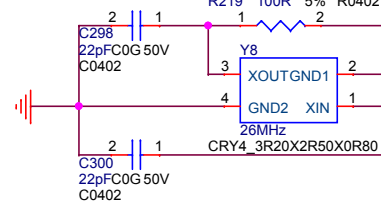


WOL/LED0 Selection

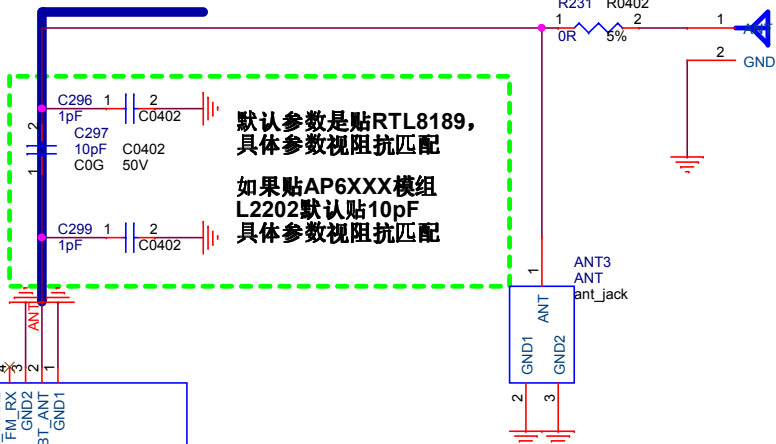
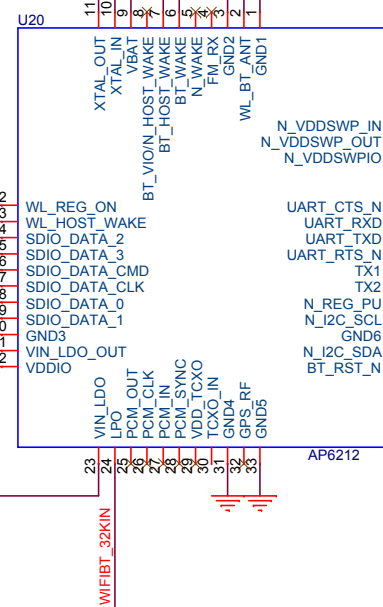
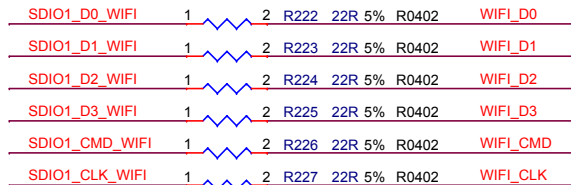


Pull Low for LED0 Mode(default)
Pull High for WOL Mode
WOL:Wake-on-LAN





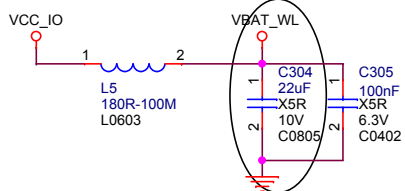
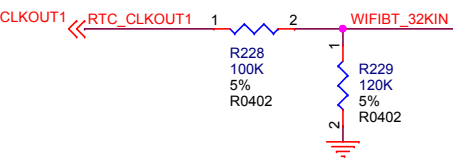
Note:
Adjusted the load capacitance
according to the crystal specification.



**默认参数是贴RTL8189，
具体参数视阻抗匹配**

如果贴AP6XXX模组
L2202默认贴10pF
具体参数视阻抗匹配

Note:
RTL8189时
PIN34是WIFI CS功能.
PIN12是悬空脚.



峰值最大有600mA



